

Title: FPGA Design Engineer

Company Description

Great people working together in a relaxed, positive environment, solving challenging engineering problems while developing world-class products

Founded in 1998, Escape Communications designs, produces and sells complex wireless communications products and systems into the telecommunications and SATCOM equipment industries, in both commercial and defense markets.

We are located in the South Bay area, a few short miles from Los Angeles County beaches. Escape is a small company, where employees get the opportunity to play a critical role in the development and delivery of world-class products to companies across the globe. We are growing, and we are looking for exceptional, motivated individuals to grow with us.

For more details, visit <http://www.escapecom.com/open-positions/>

Job Description / Required Skills

- Job involves porting, updating, and testing VHDL modules from existing FPGA-based radios and integrating them into new radios targeting similar waveforms on new platforms.
- Must have experience in basic RTL design, handling multiple clock-domains, IP integration, and hierarchical design flow.
- Must have experience coding in VHDL. All modules and test-benches will be done in VHDL-2008. Escape does NOT use Verilog.
- Must have experience developing self-checking test-benches written in VHDL. Escape develops and tests against behavioral models and test-vectors. Escape does NOT use UVM.
- Must have experience with FPGA design flow. Escape uses Altera & Xilinx devices and Quartus & Vivado for FPGA development.
- Must have experience with VHDL simulation. Escape uses ModelSim for simulation.
- Must have basic understanding of networking and packet processing functions, e.g., basic Ethernet, Internet Protocol, encapsulation/decapsulation, filtering, formatting, buffering, and forwarding.
- Must have lab experience and be proficient at debugging modules and making them work on target platforms.
- DSP and communication processing experience are beneficial but are NOT a requirement. Some knowledge of tuning, filtering, FFTs, modulation/demodulation, and encoding/decoding is beneficial.
- C, C++, scripting, and Python are beneficial but are NOT a requirement. These are beneficial to aid the software development team in developing software for configuration and status of the VHDL modules as well as to support development, diagnostics, and troubleshooting.

Benefits

Escape Communications offers a relaxed work environment and a generous benefits package, including health benefits, long-term disability insurance, flexible vacation time, a 401K retirement plan, flexible work schedule (9/80), and a stock options plan for qualified applicants.

Escape Communications is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, sexual orientation, gender identity, or national origin.

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www.escapecom.com