

Title: Test Verification and Design Engineer

Company Description

Great people working together in a relaxed, positive environment, solving challenging engineering problems while developing world-class products

Founded in 1998, Escape Communications designs, produces and sells complex wireless communications products and systems into the telecommunications and SATCOM equipment industries, in both commercial and military markets.

We are located in the South Bay area, a few short miles from Los Angeles County beaches. Escape is a small company, where employees get the opportunity to play a critical role in the development and delivery of world-class products to companies across the globe. We are growing, and we are looking for exceptional, motivated individuals to grow with us.

For more details, visit www.escapecom.com.

Escape is hiring! <http://www.escapecom.com/open-positions/>

Job Description

Escape is seeking a talented and motivated individual to lead verification and test planning and execution activities across all of our product lines, as well as contribute to the design of our product platforms. Responsibilities include:

- Test and verification plan definition and related documentation addressing design validation as well as production acceptance testing
- Development, implementation and automation of test hardware and software
- Execution of design validation testing and verification, to include documentation of results
- Contributing to product design in the areas of hardware design, FPGA implementation (VHDL) and/or embedded software development

Qualifications

The position requires the following:

- 2+ year experience in electronic systems verification and test
- 2+ years experience in test automation implementation, preferably using Labview or similar tools
- Basic working knowledge of lab equipment such as oscilloscopes, spectrum analyzers, signal sources, power supplies
- Self-motivated individual, willing to learn, and interested in working in a team environment
- Excellent technical writing skills

The following skillsets are desired, although not strictly required:

- Ability to read and understand printed circuit board schematic designs
- Experience with Intel (Altera)/Xilinx FPGA design and development, including experience in writing VHDL or Verilog
- Experience in embedded software development
- Experience in printed circuit board design

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A bachelor's degree in EE, CE or CS is required as a minimum. Master's degree and PhD holders are also welcome. Preference will be given to applicants with prior relevant experience.

Successful applicant must be able to provide proof of authorization to work full-time in the US without visa sponsorship.

Benefits

Escape Communications offers a relaxed work environment and a generous benefits package, including health benefits, flexible vacation time, optional 9/80 work schedule, a 401K retirement plan, and an incentive stock options plan for qualified applicants.

Escape Communications is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, sexual orientation, gender identity, or national origin.