escapecommunications

Title: Digital Design Engineer

Company Description

Great people working together in a relaxed, positive environment, solving challenging engineering problems, developing world-class products

Escape Communications designs, produces and sells complex wireless communications products and systems into the telecommunications equipment and satellite communications industries. We are located in the South Bay area, a few short miles from Los Angeles County beaches. Escape is a small company, where employees get the opportunity to wear many hats and learn many disciplines. We are growing, and we are looking for exceptional, motivated individuals to grow with us. We are currently looking for engineers with expertise in communications signal processing and software development.

Job Description

Escape Communications develops state-of-the-art signal processing platforms for terrestrial microwave, millimeterwave and SATCOM wireless radio links. These platforms employ highly flexible architectures, with configurable modulation modes ranging from FSK to 4096QAM, and configurable bit rates ranging up to 10 gigabits per second and beyond. Advanced features include cross-polarization interference canceling, diversity combining, adaptive modulation & coding, and advanced forward error correction. Flexible Mux and Demux functions allow the combining of multiple line interface types and channels within a single radio link. Our military SATCOM terminal platform products employ complex protected frequency hopped, time permuted TDMA waveforms. Applications include cellular backhaul, broadband wireless access backhaul, high capacity last mile, private networks, as well as commercial and military satellite communications.

Escape is seeking a talented and motivated individual to be a key contributor in signal processing development and implementation on our radio modem products. Responsibilities include:

- Design and implementation of FPGA-based digital signal processing systems
- Development of self-checking unit/system-level test benches
- Troubleshooting and resolving design issues

Qualifications

The position requires the following:

- 1-5 years of experience with FPGA design and/or verification with hardware description languages (such as VHDL or Verilog).
- Basic understanding of FPGA design flow (synthesis, place & route, timing closure, etc.)
- Experience with Mentor Graphics Modelsim or Questasim.
- Ability to read and understand schematic designs.
- Basic working knowledge of lab equipment such as oscilloscopes, spectrum analyzers, signal sources, power supplies.
- Self-motivated individual, willing to learn, and interested in working in a team environment.

The following skillsets are desired, although not strictly required:

- Experience with Altera/Xilinx FPGA design and development.
- Experience with AMBA/Avalon architectures, embedded processors, and IP cores.
- Experience with Python, Labview, or MATLAB for data collection and analysis.

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A bachelor's degree in is required as a minimum; a master's degree is strongly preferred. Preference will be given to applicants with prior relevant experience.

Successful applicant must be able to provide proof of authorization to work full-time in the US without visa sponsorship.

Benefits

Escape Communications offers a relaxed work environment and a generous benefits package, including health benefits, long-term disability insurance, flexible vacation time, a 401K retirement plan, an optional 9/80 work week, and a stock options plan for qualified applicants.

Escape Communications is an equal opportunity employer.